



Praktyczny przewodnik po symulacji i implementacji FPGA w projektowaniu cyfrowym

Indeks: **712080** Producent: **Springer**

Cena: **806.62 zł**

Opis

A Practical Guide for Simulation and FPGA Implementation of Digital Design

Producent: Springer

- **temat:** COMPUTERS / Computer Architecture, Computers/Computer Architecture, TECHNOLOGY & ENGINEERING / Electronics / Circuits / General, Technology & Engineering/Electronics - Circuits - General, HC/Informatik, EDV/Informatik, HC/Technik/Elektronik, Elektrotechnik, Nachrichtentechnik, Hardcover, Softcover / Technik/Elektronik, Elektrotechnik, Nachrichtentechnik, Circuits & components, Computer architecture & logic design, TECHNOLOGY & ENGINEERING / Electronics / Circuits / General, TECHNOLOGY & ENGINEERING, Electronics, Circuits, General, COMPUTERS / Computer Architecture, COMPUTERS, Computer Architecture, Circuits and Systems, Processor Architectures, Electronic Circuits and Systems, Electronics Design and Verification, HC/Technik/Elektronik, Elektrotechnik, Nachrichtentechnik, HC, Technik, Elektronik, Elektrotechnik, Nachrichtentechnik, HC/Informatik, EDV/Informatik, Informatik, EDV, Informatik, Field Programmable Gate Arrays (FPGA) Architecture;Application-specific integrated circuit (ASIC) Architecture;VHDL Language;Design Digital Circuits;Combinational Logic Circuits;Logic Design;Circuit Design;Logic Synthesis;Finite State Machines;Memory Circuits, Electronics: circuits and components, Computer architecture and logic design, Circuits & components, Computer architecture & logic design, Computer architecture and logic design, Electronics: circuits and components, Rechnerarchitektur und Logik-Entwurf, Schaltkreise und Komponenten (Bauteile)
- **wiązący:** paperback
- **język:** english, english, english
- **waga przedmiotu:** 528 grams
- **strony:** 344
- **słowo kluczowe tematu:** Field Programmable Gate Arrays (FPGA) Architecture; Application-specific integrated circuit (ASIC) Architecture; VHDL Language; Design Digital Circuits; Combinational Logic Circuits; Logic Design; Circuit Design; Logic Synthesis; Finite State Machines; Memory Circuits, Field Programmable Gate Arrays (FPGA) Architecture;Application-specific integrated circuit (ASIC) Architecture;VHDL Language;Design Digital Circuits;Combinational Logic Circuits;Logic Design;Circuit Design;Logic Synthesis;Finite State Machines;Memory Circuits, FieldProgrammableGateArrays(FPGA)Architecture; Application-specificintegratedcircuit(ASIC)Architecture; VHDLLanguage; DesignDigitalCircuits; CombinationalLogicCircuits; LogicDesign; circuitdesign; LogicSynthesis; finitestatemachines; MemoryCircuits
- **kod podmiotu:** 1632, 1684, 1684, UYF, TJFC, UYF, TJFC, COM011000, COM011000, TEC008010, TEC008010, TJFC, UYF
- **grupa docelowa:** General/trade
- **Liczba przedmiotów:** 1
- **kolor:** White
- **waga opakowania przedmiotu:** 1.16 pounds
- **wydanie:** 1st ed. 2022

- **producent:** Springer
- **tytuł serii:** Computer Architecture and Design Methodologies
- **zewnętrznie przypisany identyfikator produktu:** 9789811906176, 09789811906176
- **autor:** Hajji, Bekkay, Mellit, Adel, Bouselham, Loubna
- **gatunek muzyczny:** Circuits & components, Computer architecture & logic design, TECHNOLOGY & ENGINEERING, Electronics, Circuits, General, COMPUTERS, Computer Architecture, HC, Technik, Elektronik, Elektrotechnik, Nachrichtentechnik, HC, Informatik, EDV, Informatik, Electronics: circuits and components, Computer architecture and logic design
- **Data publikacji:** 2023-03-23T00:00:01Z
- **numer wydania:** 1
- **nazwa przedmiotu:** A Practical Guide for Simulation and FPGA Implementation of Digital Design
- **data premiery:** 2023-03-23T00:00:01Z
- **data uruchomienia strony produktu:** 2022-10-07T00:00:01Z

Parametry

Wydanie	1 edycja 2022
Ilość stron	344
Waga	528 gramów